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| APPLICANTS Sadami Takeoka, Osaka, JAPAN; Mitsuyasu Ohta, Osaka, JAPAN; Osamu Ichikawa, Osaka, JAPAN; Masayoshi Yoshimura, Kyoto, JAPAN; *** CONTINUING DATA ****************************** This application is a DIV of 10/187,269 07/02/2002 PAT 6,734,549 *** FOREIGN APPLICATIONS ************************************ | | | | | | | | | |
| Foreign Priority claimed yes no 35 USC 119 (a-d) conditions yes no Met after Allowance Verified and Acknowledged Examiner's Signature Initials ADDRESS | | | | STATE OR COUNTRY JAPAN | J | | TOTA CLAI 8 | | INDEPENDENT CLAIMS 4 |
| TITLE SEMICONDUCTOR WIRING SUBSTRATE, SEMICONDUCTOR DEVICE, METHOD FOR TESTING SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING SEMICONDUCTOR DEVICE | | | | | | | | | |
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